# HMS DRIFT CHAMBER *1190 TDC* CHANNEL ASSIGNENTS

| Slot | 127–111 | 95–79 | 112–96 | 80–64 | 63–47 | 31–15 | 48–32 | 16–0 |
|---|---|---|---|---|---|---|---|---|
| 2 | #N/A | 2U2:64-49 (Ch2#38) | D | C | #N/A | 1U1:81-96 (Ch1#20) | B | A |
| | #N/A | 2V1:81-96 (Ch2#29) | | | #N/A | 1V2:64-49 (Ch1#10) | | |
| 4 | 2U2:48-33 (Ch2#36) | 2U1:32-17 (Ch2#37) | D | C | 1X1:92-102,***** (Ch1#18) | 1U2:81-96 (Ch1#19) | B | A |
| | 2X2:92-102,****,R (Ch2#27) | 2V2:81-96 (Ch2#28) | | | 1V2:48-33 (Ch1#8) | 1V1:32-17 (Ch1#9) | | |
| 5 | 2V1:65-80 (Ch2#24) | 2X2:60-75 (Ch2#25) | D | C | 1X1:60-75 (Ch1#16) | 1X1:76-91 (Ch1#17) | B | A |
| | 2U2:32-17 (Ch2#35) | 2X2:76-91 (Ch2#26) | | | 1V2:32-17 (Ch1#6) | 1X2:59-44 (Ch1#7) | | |
| 7 | 2X1:11-1,****,R (Ch2#32) | 2X1:27-12 (Ch2#33) | D | C | 1X1:44-59 (Ch1#14) | 1U1:65-80 (Ch1#15) | B | A |
| | 2V1:49-64 (Ch2#23) | 2X1:43-28 (Ch2#34) | | | 1X2:27-12 (Ch1#4) | 1X2:43-28 (Ch1#5) | | |
| 8 | 2U2:16-1 (Ch2#30) | 2U1:16-1 (Ch2#31) | D | C | 1U2:65-80 (Ch1#12) | 1U1:49-64 (Ch1#13) | B | A |
| | 2V1:33-48 (Ch2#21) | 2V2:65-80 (Ch2#22) | | | 1V1:16-1 (Ch1#2) | 1X2:11-1,***** (Ch1#3) | | |
| 10 | 2U2:81-96 (Ch2#19) | 2U1:81-96 (Ch2#20) | D | C | 1U2:64-49 (Ch1#38) | 1U1:33-48 (Ch1#11) | B | A |
| | 2V1:32-17 (Ch2#9) | 2V2:64-49 (Ch2#10) | | | 1V1:81-96 (Ch1#29) | 1V2:16-1 (Ch1#1) | | |
| 13 | 2X1:76-91 (Ch2#17) | 2X1:92-102,****,R (Ch2#18) | D | C | 1U2:48-33 (Ch1#36) | 1U1:32-17 (Ch1#37) | B | A |
| | 2X2:59-44 (Ch2#7) | 2V2:48:33 (Ch2#8) | | | 1X2:92-102,***** (Ch1#27) | 1V2:81-96 (Ch1#28) | | |
| 14 | 2U1:65-80 (Ch2#15) | 2X2:60-75 (Ch2#16) | D | C | 1V1:65-80 (Ch1#24) | 1X2:60-75 (Ch1#25) | B | A |
| | 2X2:43-28 (Ch2#5) | 2V2:32-17 (Ch2#6) | | | 1U2:32-17 (Ch1#35) | 1X2:76-91 (Ch1#26) | | |
| 16 | 2U1:49-64 (Ch2#13) | 2X1:44-59 (Ch2#14) | D | C | 1X1:11-1,***** (Ch1#32) | 1X1:27-12 (Ch1#33) | B | A |
| | 2X2:11-1,****,R (Ch2#3) | 2X2:27-12 (Ch2#4) | | | 1V1:49-64 (Ch1#23) | 1X1:43-28 (Ch1#34) | | |
| 17 | 2U1:33-48 (Ch2#11) | 2U2:65-80 (Ch2#12) | D | C | 1U2:16-1 (Ch1#30) | 1U1:16-1 (Ch1#31) | B | A |
| | 2V2:16-1 (Ch2#1) | 2V1:16-1 (Ch2#2) | | | 1V1:33-48 (Ch1#21) | 1V2:65-80 (Ch1#22) | | |

**KEY:** ***CPP:ww-ww (ChC#xx)***

**C = Chamber Number (1 or 2)**

**PP= Plane ID (one of U1, U2, X1, X2, V1, V2)**

**ww-ww = Range of wire numbers.**

**(ChC#*xx*) = Ribbon Cable Identifier**

**Notes:**

1) Input signals are listed in order of increasing TDC channel number.
   Example: the **64th wire of 2U2** goes into **channel 80** of the 1190 module in Slot 2.
2) "**R**" indicates a TDC channel that has a reference-time signal fed into it
3) "*****" = a TDC channel with no input signal.